



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Taylor R. Efland

Serial No.: 09/975,630

Filed: 10/12/01

For: Circuit Structure Integrating the Power Distribution Functions of Circuits and Leadframes into the Chip Surface

Docket: TI-31678

Art Unit: 2826

Examiner: Andujar, L.

#6  
3/7/03  
1/3

EXTENSION OF TIME

February 24, 2003

Assistant Commissioner For Patents  
Washington, DC 20231

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this date the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, DC 20231.

*Michael K. Skrehot*  
Michael K. Skrehot, Reg. No. 36,682

2-24-03  
Date

Dear Sir:

Pursuant to 37 CFR 1.136(a), Applicants respectfully petitions the Commissioner for an extension of the shortened statutory period for response in the above-identified Application.

The fee for this extension is indicated below:

<input checked="" type="checkbox"/> One Month (\$110)	<input type="checkbox"/> Four Months (\$1,450)
<input type="checkbox"/> Two Months (\$410)	<input type="checkbox"/> Five Months (\$1,970)
<input type="checkbox"/> Three Months (\$930)	

Any further necessary extension of time is hereby requested. Charge any and all fees to the deposit account of Texas Instruments Incorporated, Account No. 20-0668. An original and two copies of this sheet are enclosed.

Respectfully submitted,

*Michael K. Skrehot*

Michael K. Skrehot  
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